

FIG. 1 is a block diagram of a storage system 106 connected to a host 100 via a network 105. The host 100 includes a processor 102. The storage system 106 includes an array controller 110, an interface adapter 120, and a cache 112. The array controller 110 is connected to the interface adapter 120 via a bus 114. The interface adapter 120 is connected to a network 105 via a network interface 112. The array controller 110 is also connected to a storage array 130, which includes four storage devices 130a, 130b, 130c, and 130d. The storage array 130 is connected to the interface adapter 120 via a bus 134.

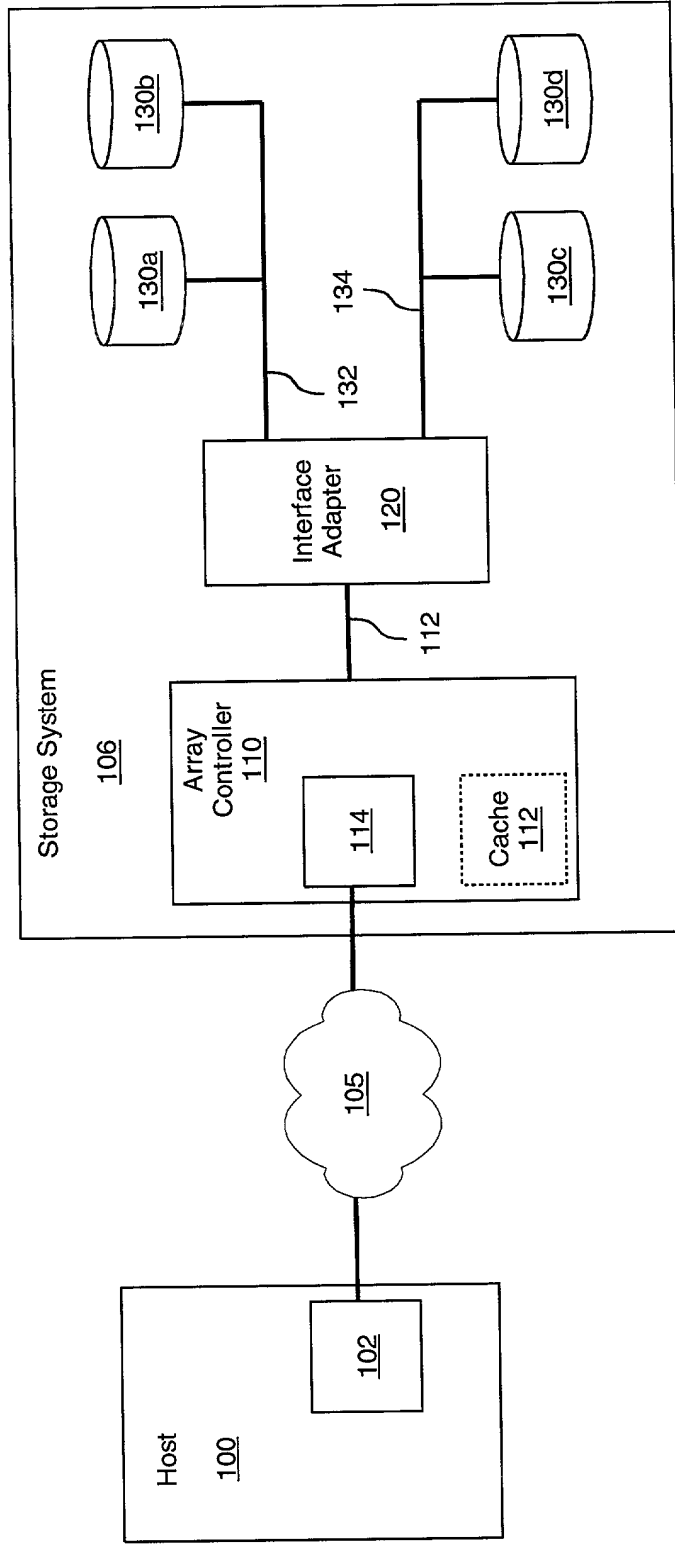


FIG. 1

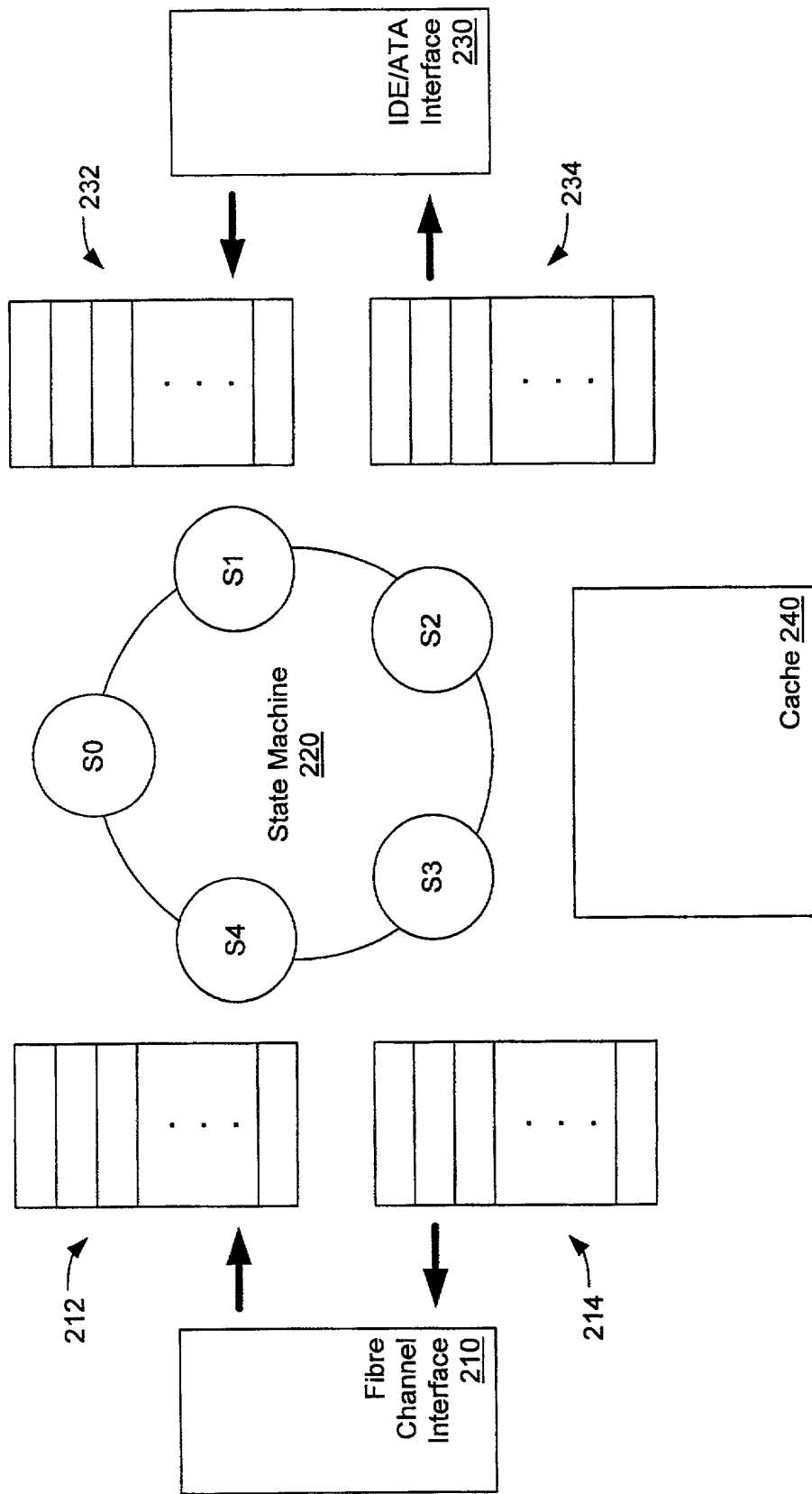


Fig. 2

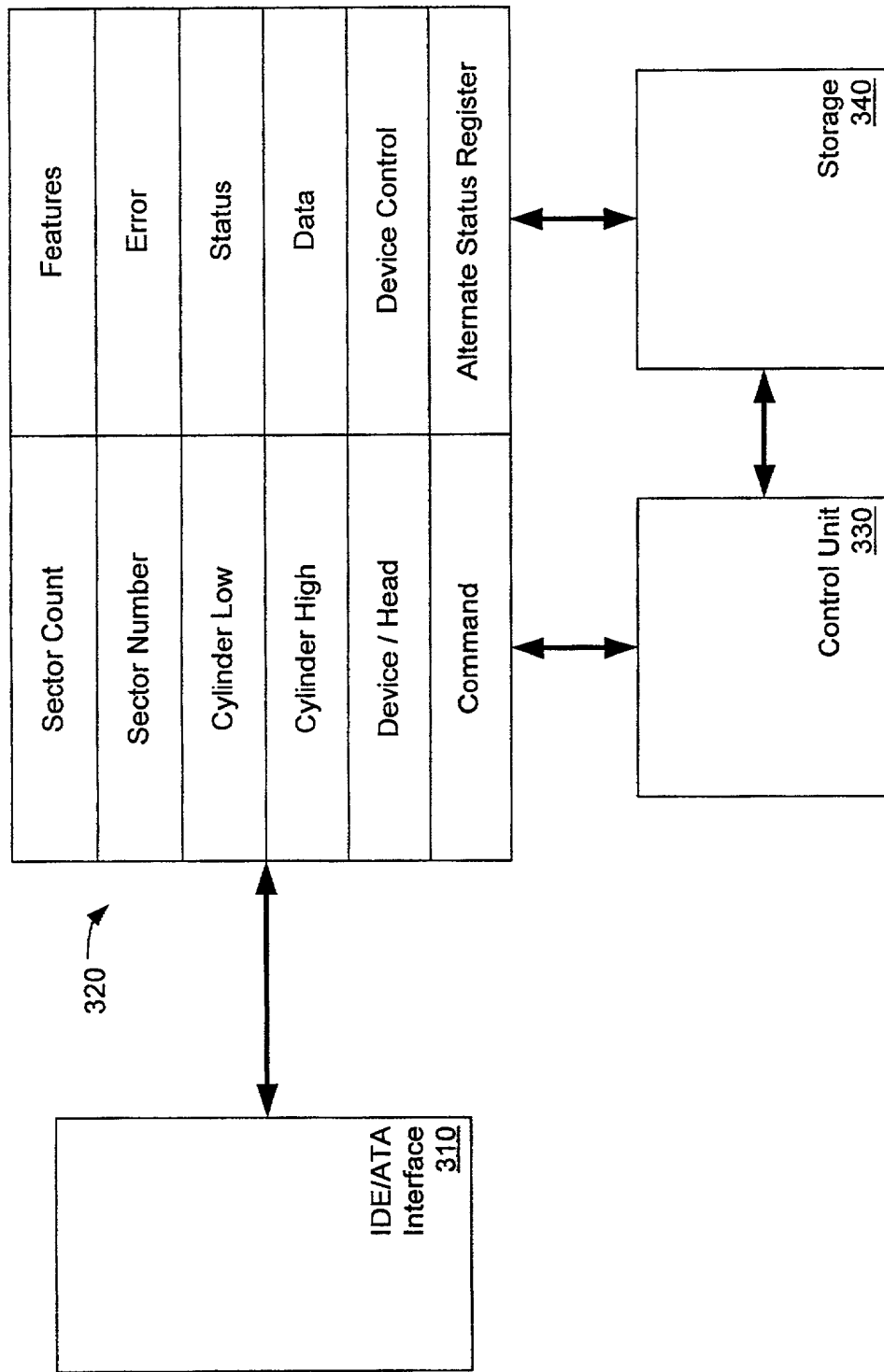


Fig. 3

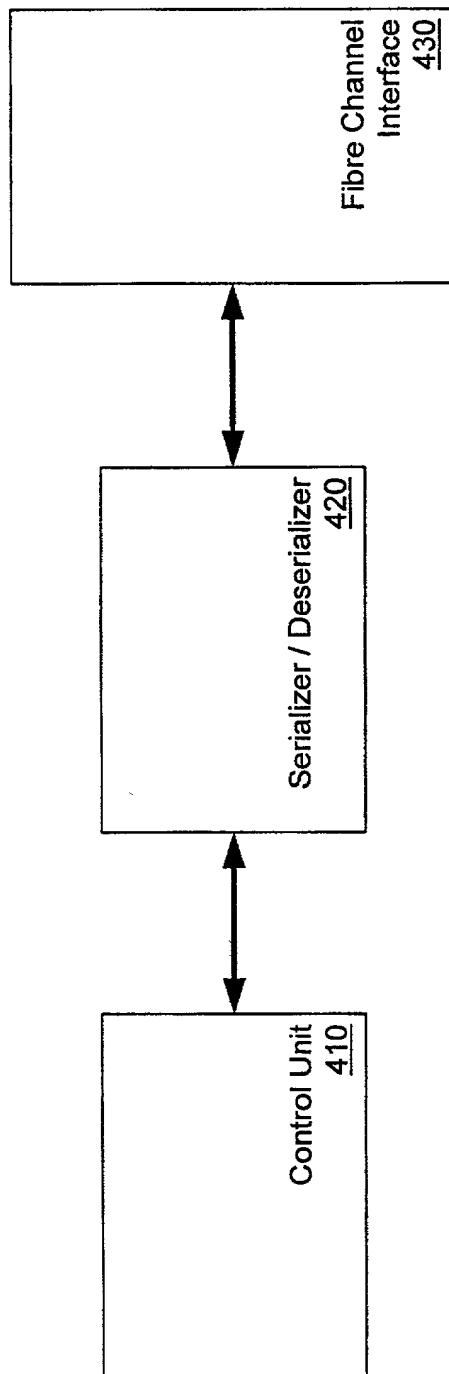


Fig. 4

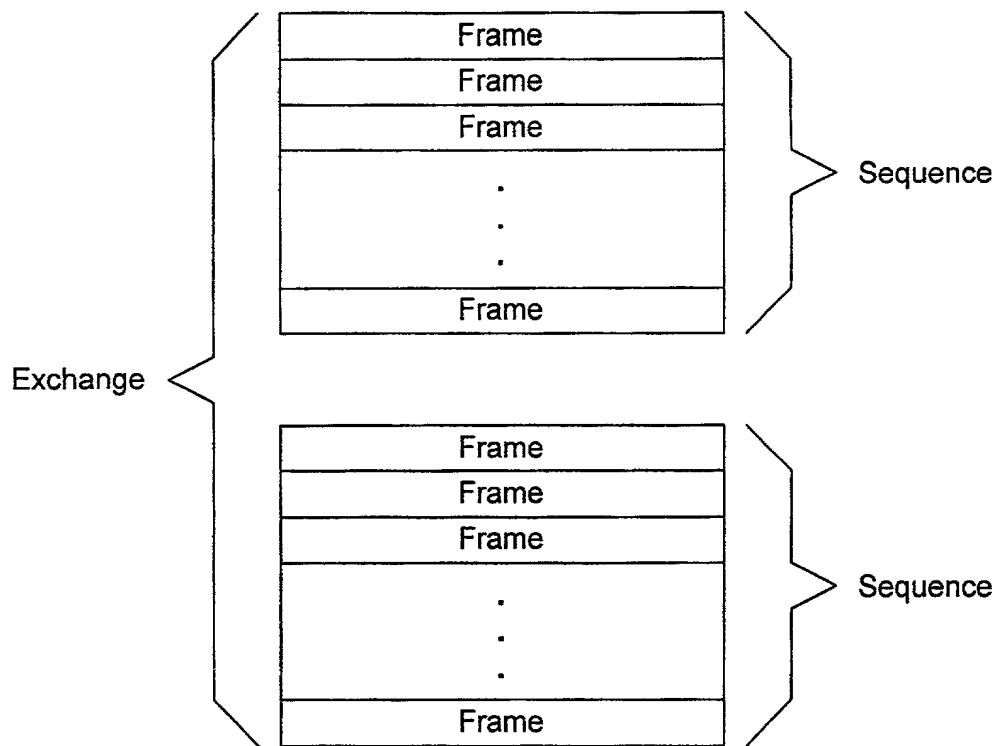


Fig. 5

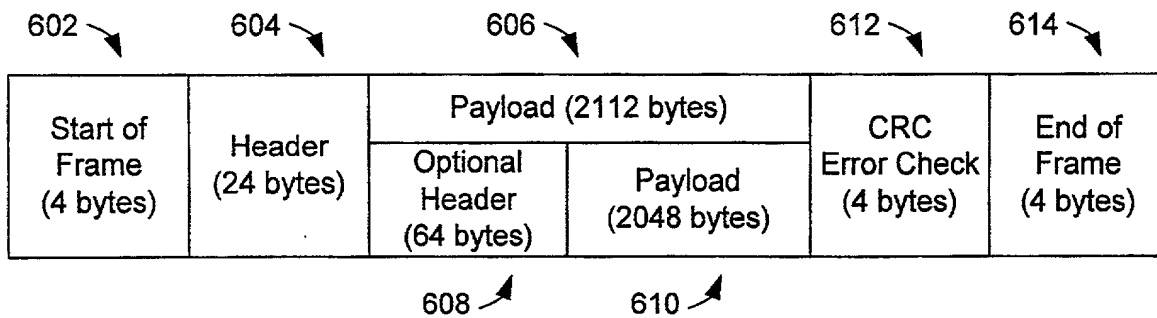


Fig. 6

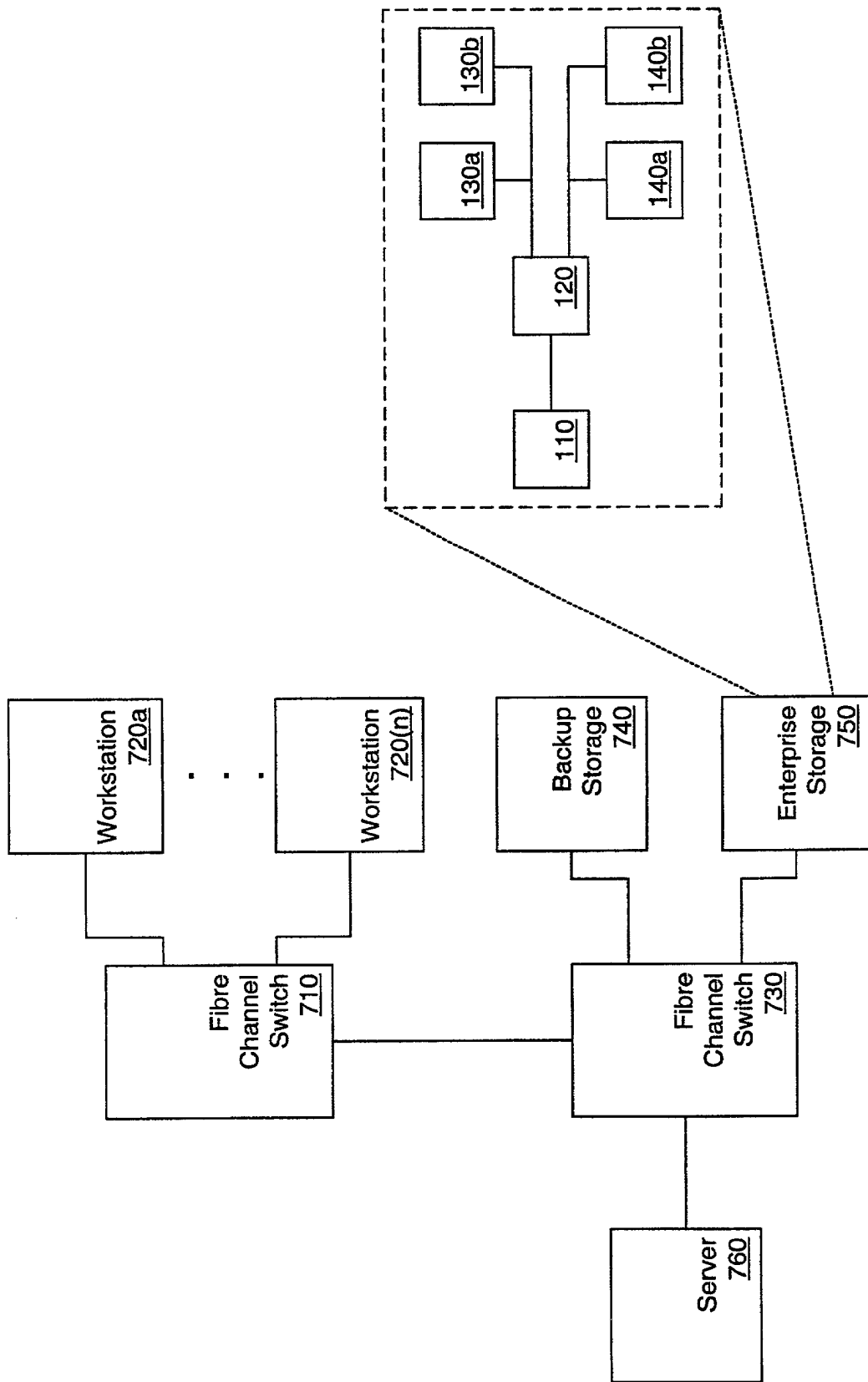


Fig. 7

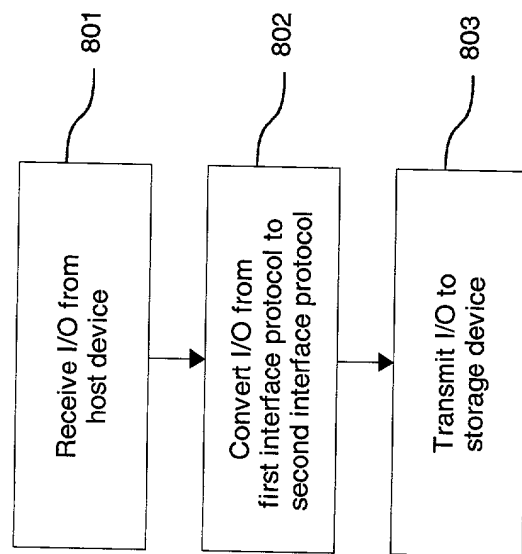


FIG. 8